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Sir:

Transmitted herewith for filing is the patent application of:

INVENTOR: Itaru KANNO, Naoki YOKOI, Hiroshi MORITA, Naoki ICHIKI, Hideaki NEZU, Masayuki TAKASHIMA
FOR: CLEANING AGENT FOR SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING SEMICONDUCTOR DEVICE

Enclosed are:

- ☒ 20 pages of specification, claims, abstract.
- ☒ Declaration and Power of Attorney.
- ☒ Priority Claimed.
- ☒ Certified copy of Japanese Patent Application No. 11-285515
- ☒ 8 sheets of formal drawing.
- ☒ An assignment of the invention to Mitsubishi Denki Kabushiki Kaisha and Sumitomo Chemical Company, Limited
- ☐ and the assignment recordation fee.
- ☐ An associate power of attorney.
- ☒ A verified statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.27.
- ☒ Information Disclosure Statement, Form PTO-1449 and reference.
- ☐ Return Receipt Postcard

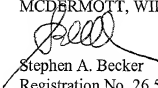
The filing fee has been calculated as shown below:

	NO. OF CLAIMS		EXTRA CLAIMS	RATE	AMOUNT
Total Claims	17	-20	0	\$18.00	\$0.00
Independent Claims	2	-3	0	\$80.00	\$0.00
Multiple Dependent Claim(s)					\$0.00
Basic Fee					\$710.00
Total of Above Calculations					\$710.00
Less ½ for Small Entity					\$0.00
Assignment & Recording Fee					\$40.00
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Respectfully submitted,

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TITLE OF THE INVENTION

Cleaning Agent for Semiconductor Device and Method of Fabricating Semiconductor Device

BACKGROUND OF THE INVENTION

5 Field of the Invention

The present invention generally relates to a cleaning agent, and more specifically, it relates to a cleaning agent for cleaning a semiconductor substrate subjected to dry etching. The present invention also relates to a method of fabricating a semiconductor device including a step of cleaning a semiconductor substrate subjected to dry etching with such a cleaning agent.

10 Description of the Prior Art

The pattern of a semiconductor integrated circuit device is increasingly refined in order to improve the speed and the performance of the device. Not only a transistor part influencing the performance of the device but also a capacitor and a multilayer wiring step are refined.

15 In order to form a fine pattern by dry etching employing a resist film as a mask, refinement of the resist pattern and dry etching having higher anisotropy are required. Consequently, a large amount of resist residue adheres onto the fine pattern after ashing for removing the resist film employed for dry etching, and it is difficult to remove the residue with a conventional cleaning liquid.

20 Further, the target grain size of particles remarkably influencing the yield of the device is also reduced following refinement of the pattern, and it is necessary to remove smaller particles.

25 At present, a mixed solution (hereinafter abbreviated as APM) of ammonia and aqueous hydrogen peroxide is widely employed for removing the resist residue and the particles in general. APM, which is an alkaline cleaning liquid, has an effect of finely etching a device material such as a silicon oxide and removing the resist residue and the particles. It has been reported in a learned society or the like that particles hardly adhere to a silicon substrate in an alkaline aqueous solution since the silicon substrate and the particles are negatively charged, and this phenomenon is

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regarded as the reason for the high cleaning effect of APM.

In a device following a design rule of not more than 0.15 μm , wires for a gate electrode, a bit line and the like are prepared from a metal material such as tungsten or a tungsten alloy having low resistance.

5 Tungsten or an alloy such as tungsten nitride is dissolved due to reaction with aqueous hydrogen peroxide contained in APM. If such a material is exposed, therefore, APM cannot be used. When employing a mixed solution (dilute aqueous ammonia) of ammonia and water containing no aqueous hydrogen peroxide as a cleaning liquid for preventing dissolution of the material, a silicon material such as silicon forming a semiconductor substrate or polysilicon or amorphous silicon employed as a wire material or an electrode material for a capacitor is dissolved due to reaction with ammonia although dissolution of tungsten is suppressed.

10 While alkaline cleaning liquids include an aqueous solution (generally used as a developer) of tetramethylammonium hydroxide (TMAH), a water-soluble organic solvent containing organic amine and the like in addition to the aforementioned aqueous ammonia, all these cleaning liquids dissolve silicon and have low cleaning ability.

15 Thus, there is no proper cleaning liquid removing a resist residue and particles in a state simultaneously exposing tungsten or an alloy such as tungsten nitride and silicon under the present circumstances.

20 Problems of a conventional method of fabricating a semiconductor device having a step simultaneously exposing tungsten and silicon are now described.

25 Fig. 2 is a sectional view of a semiconductor device fabricated with a conventional cleaning agent. An isolation insulator film 2 for isolating element regions from each other is provided on a major surface of a semiconductor substrate 1. A gate electrode 6 is formed by stacking a polysilicon film 4 and a tungsten (or tungsten alloy) film 5 on the semiconductor substrate 1 through a gate insulator film 3. An interlayer isolation film 7 is formed on the semiconductor substrate 1 to cover the gate electrode 6. A connection hole 7a exposing the surface of the semiconductor substrate 1 and a connection hole 7b reaching the surface of

the gate electrode 6 are formed in the interlayer isolation film 7, and embedded conductive layers 8 consisting of tungsten are embedded in the connection holes 7a and 7b respectively. A bit line 9 consisting of tungsten or a tungsten alloy is provided on the interlayer isolation film 7, to be
5 connected with the embedded conductive layers 8. Interlayer isolation films 10 and 11 are provided on the interlayer isolation film 7, to cover the bit line 9. A connection hole 12a exposing the surface of the semiconductor substrate 1 is formed through the interlayer isolation films 11, 10 and 7. An embedded conductive layer 12 consisting of tungsten is provided to cover the side wall surface and the bottom surface of the connection hole 12a. This embedded conductive layer 12, which must essentially be completely embedded in the connection hole 12a, is not completely embedded in the prior art.

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15 An aluminum wiring layer 13 consisting of aluminum or an aluminum alloy is provided on the interlayer isolation film 11, to be connected with the embedded conductive layer 12. The conventional semiconductor device shown in Fig. 2 is fabricated through steps shown in Figs. 3 to 15.

20 The problems of the method of fabricating the conventional semiconductor device shown in Fig. 2 are now described.

First Problem

Tungsten or a tungsten alloy is used as an electrode material in the conventional semiconductor device, and a gate electrode made of such a material is generally referred to as a metal gate. Tungsten silicide (WSi)
25 was employed as a previous electrode material.

Referring to Fig. 3, the isolation insulator film 2 is formed on the semiconductor substrate 1. Then, the surface of the semiconductor substrate 1 is oxidized for forming the gate insulator film 3. The polysilicon film 4 and the tungsten film 5 serving as electrode materials are successively formed on the gate insulator film 3, and a resist pattern 14 is formed thereon.
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Referring to Fig. 4, the resist pattern 14 is employed as a mask for performing reactive ion etching (dry etching) on the tungsten film 5 and the

polysilicon film 4, thereby forming the gate electrode 6.

Referring to Figs. 4 and 5, the resist pattern 14 is removed by plasma treatment (referred to as ashing) with a gas containing oxygen. At this time, upwardly extending resist residues 15 adhere to the side walls of the gate electrode 6.

Referring to Figs. 5 and 6, the resist residues 15 are removed by treatment with a cleaning agent. The cleaning agent is prepared from a mixed solution (dilute aqueous ammonia) of ammonia and water. The dilute aqueous ammonia, having small dissolubility for tungsten, dissolves polysilicon due to reaction with ammonia. Therefore, the polysilicon film 4 forming the gate electrode 6 is transversely etched to narrow the width of the gate electrode 6. Consequently, the electric characteristics of a transistor are disadvantageously deteriorated. When the cleaning agent is prepared from APM, the tungsten film 5 is remarkably dissolved although the polysilicon film 4 is not etched. Thus, APM cannot be used in practice.

While this prior art is described with reference to dry etching employing a resist mask, a similar problem arises also when employing a silicon nitride film as a mask.

Second Problem

A second problem of the method of fabricating the conventional semiconductor device having a step simultaneously exposing tungsten and silicon is now described.

Referring to Fig. 7, the gate electrode 6 is formed on the semiconductor substrate 1 and the interlayer isolation film 7 is formed thereon. A resist pattern 14 is formed on the interlayer isolation film 7.

Referring to Fig. 8, the resist pattern 14 is employed as a mask for performing reactive ion etching (dry etching) on the interlayer isolation film 7, thereby forming contact holes 16 in the interlayer isolation film 7.

Referring to Figs. 8 and 9, the resist pattern 14 is removed by plasma treatment with a gas containing oxygen. At this time, upwardly extending resist residues 15 adhere to the side walls of the contact holes 16 as shown in Fig. 9.

Referring to Figs. 9 and 10, the resist residues 15 are removed by

treatment with a cleaning agent, which is prepared from dilute aqueous ammonia. With this cleaning agent, silicon forming the semiconductor substrate 1 is isotropically etched to form depressions 17 on the bottoms of the contact holes 16 although the cleaning agent has small dissolubility for the tungsten film 5. When such depressions 17 are formed, the embedded conductive layers 8 are not completely embedded in the contact holes 16 as shown in Fig. 2 but the bit line 9 may be disconnected from the gate electrode 6 or the semiconductor substrate 1. Even if no such disconnection takes place, resistance is disadvantageously increased. If the cleaning agent is prepared from APM, the tungsten film 5 is remarkably dissolved although the semiconductor substrate 1 is not etched. Therefore, APM cannot be used in practice.

While this prior art is described with reference to dry etching employing a resist mask, a similar problem arises also when employing a silicon nitride film as a mask.

Third Problem

A third problem of the prior art is now described.

Referring to Fig. 11, the gate electrode 6, the embedded conductive layers 8 and the bit line 9 are successively formed on the semiconductor substrate 1, and the interlayer isolation films 10 and 11 are formed thereon. A resist pattern 14 is formed on the interlayer isolation film 11.

Referring to Fig. 12, the resist pattern 14 is employed as a mask for forming a contact hole 18 by reactive ion etching (dry etching).

Referring to Figs. 12 and 13, the resist pattern 14 is removed by plasma treatment with a gas containing oxygen. At this time, resist residues 15 vertically extending upward adheres to the side wall of the contact hole 18.

Referring to Figs. 13 and 14, the resist residues 15 are removed by treatment with a cleaning agent.

Following higher integration and refinement of the semiconductor device, different types of insulator films are employed in a composite manner for flattening the surface of the substrate. The insulator films are formed by a thermally oxidized silicon film, a silicon oxide film prepared by

CVD, a BPSG film containing B, P etc. and the like.

In this example, the interlayer isolation films 7, 10 and 11 are made of different materials. For example, the interlayer isolation film 7 is made of TEOS, the interlayer isolation film 10 is made of BPSG, and the interlayer isolation film 11 is made of TEOS. In formation of the contact hole 18, APM is employed as the cleaning agent since no tungsten is exposed in this portion. The quantity of dissolution (quantity of etching) with APM having dissolubility for a silicon oxide film varies with the type of the silicon oxide film. APM has a larger quantity of etching for the BPSG film forming the interlayer isolation film 10 as compared with the TEOS films forming the interlayer isolation films 7 and 11. Therefore, the interlayer isolation film 10 is transversely etched on the wall surface of the contact hole 18 as shown in Fig. 14, to irregularize the side wall surface of the contact hole 18.

Referring to Figs. 14 and 15, a tungsten film is formed by CVD in this state for forming the embedded conductive layer 12 consisting of tungsten in the contact hole 18 by dry etching or chemical mechanical polishing (CMP). At this time, the contact hole 18 is not completely filled up with tungsten but defines a cavity as shown in Fig. 15, to result in disconnection in an intermediate stage as the case may be.

Consequently, the aluminum wiring layer 13 formed on the contact hole 18 as shown in Fig. 2 may not be connected with the semiconductor substrate 1. Even if no such disconnection takes place, the resistance between the aluminum wiring layer 13 and the semiconductor substrate 1 is disadvantageously increased.

When cleaned with APM, the diameter of the contact hole 18 is increased to cause short-circuiting with a wire or the like arranged beside the contact hole 18 as the case may be.

In addition to APM, dilute aqueous ammonia also has an effect of removing resist residues. However, dilute aqueous ammonia cannot be applied since the same etches the semiconductor substrate 1.

While the prior art is described with reference to contact with the semiconductor substrate 1, similar problems arise also with reference to

contact with another element such as the gate electrode 6 or the bit line 9.

As hereinabove described, the conventional cleaning agent and the conventional method of fabricating a semiconductor device have the problems of dissolving tungsten, an alloy such as tungsten nitride or silicon and causing difference between quantities of etching of different types of oxide films, to consequently deteriorate the characteristics of the semiconductor device by disconnecting the wire and the embedded conductive layer and increasing the resistance.

SUMMARY OF THE INVENTION

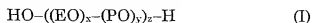
Accordingly, an object of the present invention is to provide a cleaning agent for a semiconductor device, which is so improved as not to disconnect a wire and an embedded conductive layer.

Another object of the present invention is to provide a cleaning agent for a semiconductor device, which is so improved as not to increase the resistance of a wire or an embedded layer.

Still another object of the present invention is to provide a method of fabricating a semiconductor device, which is so improved as not to disconnect a wire or an embedded layer.

A further object of the present invention is to provide a method of fabricating a semiconductor device, which is so improved as not to increase the resistance of a wire or an embedded layer.

A cleaning agent for a semiconductor device according to a first aspect of the present invention contains a hydroxide, water and a compound expressed in the following general formula (I) and/or the following general formula (II):



where EO represents an oxyethylene group, PO represents an oxypropylene group, x and y represent integers satisfying $x/(x+y) = 0.05$ to 0.4 , and z represents a positive integer.



where EO, PO, x, y and z are defined identically to those in the general formula (I), R represents a residue of alcohol or amine excluding a hydroxyl group or a hydrogen atom of an amino group, and m represents an integer

of at least 1.

The oxyethylene group is expressed as $-\text{CH}_2-\text{CH}_2-\text{O}-$, and the oxypropylene group is expressed as $-\text{CH}(\text{CH}_3)-\text{CH}_2-\text{O}-$ or as $-\text{CH}_2-\text{CH}(\text{CH}_3)-\text{O}-$.

Dissolubility in preparation of the cleaning agent is insufficient if the value of $x/(x+y)$ is less than 0.05, while defoamability of the cleaning agent is insufficient if the value is greater than 0.4.

The part expressed as $(\text{EO})_x-(\text{PO})_y$ in each of the general formulas (I) and (II) may be a block copolymer, a random copolymer or a blocky random copolymer, and the block copolymer is preferable among these.

Alcohol forming the aforementioned R is prepared from monohydric alcohol such as 2-ethylhexyl alcohol, lauryl alcohol, cetyl alcohol, oleyl alcohol, stearyl alcohol, tridecyl alcohol, tallow alcohol or coconut oil alcohol or polyhydric alcohol such as ethylene glycol, propylene glycol, 1, 3-propanediol, 1,2-butanediol, 1,3-butanediol, 2,3-butanediol, 1,4-butanediol, 2-methyl-1,2-propanediol, 2-methyl-1,3-propanediol, glycerin, trimethylol ethane, trimethylol propane, pentaerythritol or sorbitol, and amine is prepared from methylene diamine or propylene diamine.

The cleaning agent according to the first aspect of the present invention hardly dissolves tungsten or an alloy such as tungsten nitride, silicon and an insulator film, and exhibits the same quantity of etching for different types of insulator films. Consequently, the cleaning agent attains such an effect that the width of a gate electrode is not narrowed.

Preferably, the aforementioned hydroxide is ammonium hydroxide.

In this case, the amount of impurities contained in a solution is so small that no impurities remain on the surface of a semiconductor substrate since ammonium hydroxide is employed as the hydroxide. Preferably, the aforementioned hydroxide is selected from a group consisting of tetramethylammonium hydroxide, a hydroxide of potassium and a hydroxide of sodium.

The concentration of the hydroxide contained in the aforementioned cleaning agent is preferably 0.01 to 31 percent by weight, and more preferably 0.1 to 3 percent by weight. A sufficient cleaning effect cannot

be attained if the concentration of the hydroxide is excessively low, while the quantity of etching for silicon is increased if the concentration of the hydroxide is excessively high. Therefore, the concentration of the hydroxide is preferably in the range of 0.01 percent by weight to 31 percent by weight.

Preferably, the mean molecular weight of the total of the oxypropylene group in the compound expressed in the general formula (I) and/or (II) is 500 to 5000.

The cleaning effect is insufficient if the mean molecular weight is too small, while dissolubility in preparation is insufficient if the mean molecular weight is too large.

Preferably, the weight ratio of the compound expressed in the general formula (I) and/or (II) to the hydroxide is $(0.3 \times 10^{-4}$ to 1):1.

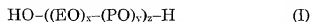
The quantity of etching for silicon is increased if the ratio of the copolymer is too small, while defoamability is insufficient if the ratio of the copolymer is too large.

Preferably, the pH of the aforementioned cleaning agent is rendered at least 8.

Preferably, the cleaning agent further contains not more than 1 percent by weight of hydrogen peroxide.

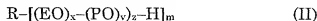
The quantity of etching for tungsten, which is increased if the content of hydrogen peroxide is large, can be reduced to a proper level if the content of hydrogen peroxide is not more than 1 percent by weight, while the quantity of etching for silicon can be further reduced due to mixing with hydrogen peroxide.

A method of fabricating a semiconductor device according to a second aspect of the present invention comprises a first step of preparing a semiconductor substrate completely subjected to dry etching and a second step of cleaning the surface of the semiconductor substrate with a cleaning agent containing a hydroxide, water and a compound expressed in the following general formula (I) and/or the following general formula (II):



where EO represents an oxyethylene group, PO represents an oxypropylene

group, x and y represent integers satisfying $x/(x + y) = 0.05$ to 0.4 , and z represents a positive integer.



where EO, PO, x, y and z are defined identically to those in the general formula (I), R represents a residue of alcohol or amine excluding a hydroxyl group or a hydrogen atom of an amino group, and m represents an integer of at least 1.

The conventional cleaning agent has such a problem that a polysilicon film forming a gate electrode is transversely etched to narrow the width of the gate electrode and deteriorate the electrical characteristics of a transistor. When employing the aforementioned cleaning agent, however, the width of the gate electrode is not narrowed since the polysilicon film is not etched.

Preferably, the aforementioned first step includes steps of performing the aforementioned dry etching with a resist pattern and removing the resist pattern by ashing.

In this case, a resist residue adhering onto a fine pattern can be efficiently removed.

Preferably, the aforementioned first step includes a step of exposing a metal film containing tungsten and/or a silicon material on the aforementioned semiconductor substrate by the aforementioned dry etching.

In this case, a resist residue and particles can be efficiently removed in such a state that tungsten or an alloy such as tungsten nitride and silicon are simultaneously exposed.

Preferably, the aforementioned first step includes steps of forming a wiring pattern containing polysilicon and tungsten on the aforementioned semiconductor substrate by the aforementioned dry etching employing a resist pattern and removing the resist pattern.

In this case, a polysilicon film forming a gate electrode is not transversely etched. Thus, the width of the gate electrode is not narrowed.

Preferably, the aforementioned first step includes steps of forming a wiring pattern containing tungsten on the aforementioned semiconductor substrate, forming an insulator film on the wiring pattern, forming a

connection hole in the insulator film by the aforementioned dry etching employing a resist pattern, and removing the resist pattern.

In this case, no depression is formed on the bottom of the connection hole. Thus, an embedded conductive layer is completely embedded in the connection hole.

Preferably, the aforementioned first step includes steps of forming at least two types of silicon oxide insulator films on the aforementioned semiconductor substrate and performing the aforementioned dry etching on the two types of silicon oxide insulator films.

In this case, the side wall surface of a contact hole is not irregularized. Thus, tungsten or the like is completely embedded in the contact hole.

Preferably, the aforementioned cleaning is performed by setting the liquid temperature of the aforementioned cleaning agent to 20°C to 50°C and dipping the aforementioned semiconductor substrate in the cleaning agent.

Removability for a resist residue is reduced if the liquid temperature is not more than 20°C, while dissolubility for silicon or an insulator film is increased if the liquid temperature is in excess of 65°C. Therefore, the liquid temperature is preferably in the range of 20°C to 65°C, for dipping the semiconductor substrate.

Preferably, the aforementioned cleaning is performed by setting the liquid temperature of the aforementioned cleaning agent to 20°C to 65°C and spraying the cleaning agent to the semiconductor substrate.

Removability for a resist residue is reduced if the liquid temperature is not more than 20°C, while dissolubility for silicon or an insulator film is increased if the liquid temperature is in excess of 65°C. Therefore, the cleaning is desirably performed by spraying at the liquid temperature in the range of 20°C to 65°C.

Preferably, the aforementioned cleaning is performed by introducing ultrasonic waves into the aforementioned semiconductor substrate.

If the cleaning is performed for removing particles or the like, the cleaning effect is further improved by employing physical cleaning with

ultrasonic waves or the like.

The foregoing and other objects, features, aspects and advantages of the present invention will become more apparent from the following detailed description of the present invention when taken in conjunction with the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a sectional view of a semiconductor device fabricated with a cleaning agent according to an embodiment of the present invention;

Fig. 2 is a sectional view of a semiconductor device formed by a conventional fabrication method; and

Figs. 3 to 15 are sectional views showing first to thirteenth steps of a conventional method of fabricating a semiconductor device.

DESCRIPTION OF THE PREFERRED EMBODIMENT

An embodiment of the present invention is now described with reference to the drawing.

Fig. 1 is a sectional view of a semiconductor device fabricated with a cleaning agent according to the embodiment of the present invention. The semiconductor device shown in Fig. 1 is identical to that shown in Fig. 2 fabricated by the conventional method except the points described below. Therefore, parts identical or corresponding to those in Fig. 1 are denoted by the same reference numerals and redundant description is not repeated.

The inventive semiconductor device shown in Fig. 1 is different from the prior art in the point that a polysilicon film 4 is not etched and hence the width of a gate electrode 6 is not narrowed.

Further, no depressions are defined by etching on the bottoms of contact holes 8.

In addition, the side wall of a contact hole 12 is not irregularized but an embedded conductive layer 12 is completely embedded in the contact hole 12.

While Examples of the present invention are now described, the present invention is not restricted to these Examples.

Examples 1, 2 and 3 and Comparative Example 1

A test piece for each of cleaning agents of Examples 1, 2 and 3 and

comparative example 1 was formed as an object of cleaning by a sample prepared by forming a silicon dioxide film of 1000 Å on a silicon substrate and thereafter forming a polysilicon film of 2800 Å in thickness. The test pieces were dipped in the cleaning agents shown in Table 1, which were held at 45°C in thermostatic baths for measuring the thicknesses of the polysilicon films before and after dipping, thereby examining the etching rates of the cleaning agents for polysilicon. Table 1 shows the results.

Table 1

	Composition of Cleaning Liquid	Etching Rate
Example 1	ammonium hydroxide: 0.3 wt.% Adeca L31*: 2.5ppm	3.0 Å/min
Example 2	ammonium hydroxide: 0.3 wt.% Adeca TR702**: 2.5ppm	1.0 Å/min
Example 3	ammonium hydroxide: 0.3 wt.% NFB2040***: 2.5ppm	2.0 Å/min
Comparative Example 1	ammonium hydroxide: 0.3 wt.%	35 Å/min

* Adeca L31 (by Asahi Denka Kogyo K.K.): a compound expressed in the general formula (I) with $x/(x+y)$ of 0.1 and a mean molecular weight of 950 of the total of the oxypropylene group

** Adeca TR702 (by Asahi Denka Kogyo K.K.): a compound expressed in the general formula (II) with $x/(x+y)$ of 0.2, a mean molecular weight of 2500 to 3000 of the total of the oxypropylene group and R of ethylene diamine

*** NFB2040 (by Aoki Oils and Fats Kogyo K.K.): a compound expressed in the general formula (II) with $x/(x+y)$ of 0.35, a mean molecular weight of 850 of the total of the oxypropylene group and R of ethylhexyl alcohol

The thicknesses of the polysilicon films were measured with Nanospeck AFT by Nanometrix Co., Ltd.

It was possible to suppress etching of the polysilicon films by employing the cleaning agents having the compositions of Examples 1, 2 and 3.

Example 4

Example 4 of the present invention is described with reference to exemplary application of the inventive cleaning agent in the process of forming a gate electrode.

First, steps similar to those shown in Figs. 3 and 4 are carried out. Referring to Figs. 5 and 6, the resist residues 15 adhering to the upper and side surfaces of the gate electrode 6 are removed with the cleaning agent according to the present invention. The inventive cleaning agent hardly has dissolubility for tungsten or an alloy such as tungsten nitride, silicon and an insulator film, and has the same etching rate for different types of insulator films. The conventional cleaning agent has the problem that the polysilicon film 4 forming the gate electrode 6 is transversely etched to narrow the width of the gate electrode 6 and deteriorate the electric characteristics of the transistor. When the inventive cleaning agent is employed, the polysilicon film 4 is not etched and hence the width of the gate electrode 6 is not narrowed as shown in Fig. 1. The hydroxide contained in the inventive cleaning agent is any of ammonium hydroxide, tetramethylammonium hydroxide, a hydroxide of potassium and a hydroxide of sodium. In consideration of impurities contained in the solution, ammonium hydroxide attaining satisfactory results in semiconductor application is most suitable.

Example 5

Example 5 of the present invention is described with reference to formation of a contact hole for connection with a gate electrode and a contact hole for connection with a semiconductor substrate.

First, steps similar to those of the prior art shown in Figs. 7 and 8 are carried out.

In the state shown in Fig. 9, the resist residues 15 adhering to the side surfaces of the contact holes 16 are removed with any of the inventive cleaning agents described with reference to Example 1. The inventive cleaning agent, hardly having dissolubility for tungsten or an alloy such as tungsten nitride and silicon, causes no depressions 17 shown in Fig. 10, which are caused in the conventional cleaning agent by etching the

semiconductor substrate 1. Consequently, the bit line 9 is normally connected with the gate electrode 6 and the semiconductor substrate 1 through the embedded conductive layers 8 as shown in Fig. 1, so that stable electric characteristics can be obtained.

Example 6

Example 6 of the present invention relates to formation of a contact hole for connection with a semiconductor substrate.

First, steps similar to those of the prior art shown in Figs. 11 and 12 are carried out.

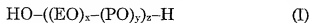
In the state shown in Fig. 13, the resist residues 15 adhering to the upper and side surfaces of the contact hole 18 are removed with the inventive cleaning agent described with reference to Example 1. The inventive cleaning agent, hardly having dissolubility for silicon and an insulator film, causes no irregularity shown in Fig. 14 caused in the conventional cleaning agent by etching the interlayer isolation film 10. Consequently, the aluminum wiring layer 13 is normally connected with the semiconductor substrate 1 through the embedded conductive layer 12 as shown in Fig. 1, so that stable electric characteristics can be obtained.

While the conventional cleaning agent entirely enlarges the diameter of the contact hole 18 to short-circuit a wire arranged beside the contact hole 18 or the like as the case may be, such entire enlargement of the diameter of the contact hole 18 is suppressed in the case of the inventive cleaning agent having a small quantity of etching for an insulator film.

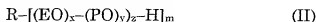
Although the present invention has been described and illustrated in detail, it is clearly understood that the same is by way of illustration and example only and is not to be taken by way of limitation, the spirit and scope of the present invention being limited only by the terms of the appended claims.

WHAT IS CLAIMED IS:

1. A cleaning agent for a semiconductor device containing a hydroxide, water and a compound expressed in the following general formula (I) and/or the following general formula (II):



where EO represents an oxyethylene group, PO represents an oxypropylene group, x and y represent integers satisfying $x/(x+y) = 0.05$ to 0.4 , and z represents a positive integer.



where EO, PO, x, y and z are defined identically to EO, PO, x, y and z in the general formula (I), R represents a residue of alcohol or amine excluding a hydroxyl group or a hydrogen atom of an amino group, and m represents an integer of at least 1.

2. The cleaning agent for a semiconductor device in accordance with claim 1, wherein said hydroxide includes ammonium hydroxide.

3. The cleaning agent for a semiconductor device in accordance with claim 1, wherein said hydroxide is selected from a group consisting of tetramethylammonium hydroxide, a hydroxide of potassium and a hydroxide of sodium.

4. The cleaning agent for a semiconductor device in accordance with claim 1, wherein the concentration of said hydroxide contained in said cleaning agent is 0.01 percent by weight to 31 percent by weight.

5. The cleaning agent for a semiconductor device in accordance with claim 1, wherein the mean molecular weight of the total of said oxypropylene group in said compound expressed in the general formula (I) or (II) is 500 to 5000.

6. The cleaning agent for a semiconductor device in accordance with

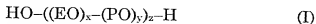
claim 1, wherein the weight ratio of said compound expressed in the general formula (I) and/or the general formula (II) to said hydroxide is $(0.3 \times 10^{-4} \text{ to } 1):1$.

7. The cleaning agent for a semiconductor device in accordance with claim 1, wherein the pH of said cleaning agent is at least 8.

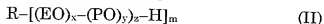
8. The cleaning agent for a semiconductor device in accordance with claim 1, further containing not more than 1 percent by weight of hydrogen peroxide.

9. A method of fabricating a semiconductor device, comprising:
a first step of preparing a semiconductor substrate completely subjected to dry etching; and

a second step of cleaning the surface of said semiconductor substrate with a cleaning agent containing a hydroxide, water and a compound expressed in the following general formula (I) and/or the following general formula (II):



where EO represents an oxyethylene group, PO represents an oxypropylene group, x and y represent integers satisfying $x/(x+y) = 0.05 \text{ to } 0.4$, and z represents a positive integer.



where EO, PO, x, y and z are defined identically to EO, PO, x, y and z in the general formula (I), R represents a residue of alcohol or amine excluding a hydroxyl group or a hydrogen atom of an amino group, and m represents an integer of at least 1.

10. The method of fabricating a semiconductor device in accordance with claim 9, wherein said first step includes steps of:
performing said dry etching with a resist pattern, and
removing said resist pattern by ashing.

11. The method of fabricating a semiconductor device in accordance with claim 9, wherein said first step includes a step of exposing a metal film containing tungsten and/or a silicon material on said semiconductor substrate by said dry etching.

12. The method of fabricating a semiconductor device in accordance with claim 9, wherein said first step includes steps of:

forming a wiring pattern containing polysilicon and tungsten on said semiconductor substrate by said dry etching employing a resist pattern, and

removing said resist pattern.

13. The method of fabricating a semiconductor device in accordance with claim 9, wherein said first step includes steps of:

forming a wiring pattern containing tungsten on said semiconductor substrate,

forming an insulator film on said wiring pattern,

forming a connection hole in said insulator film by said dry etching employing a resist pattern, and

removing said resist pattern.

14. The method of fabricating a semiconductor device in accordance with claim 9, wherein said first step includes steps of:

forming at least two types of silicon oxide insulator films on said semiconductor substrate, and

performing said dry etching on said two types of silicon oxide insulator films.

15. The method of fabricating a semiconductor device in accordance with claim 9, wherein said cleaning is performed by setting the liquid temperature of said cleaning agent to 20°C to 65°C, and

dipping said semiconductor substrate in said cleaning agent.

16. The method of fabricating a semiconductor device in accordance with claim 9, wherein said cleaning is performed by setting the liquid temperature of said cleaning agent to 20°C to 65°C, and spraying said cleaning agent to said semiconductor substrate.

17. The method of fabricating a semiconductor device in accordance with claim 9, wherein said cleaning is performed by introducing ultrasonic waves into said semiconductor substrate.

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$$\text{HO}-((\text{EO})_x-(\text{PO})_y)_z-\text{H} \quad (\text{I})$$
$$\text{R}-[(\text{EO})_x-(\text{PO})_y]_z-\text{H}]_m \quad (\text{II})$$

- 20 -

FIG. 1

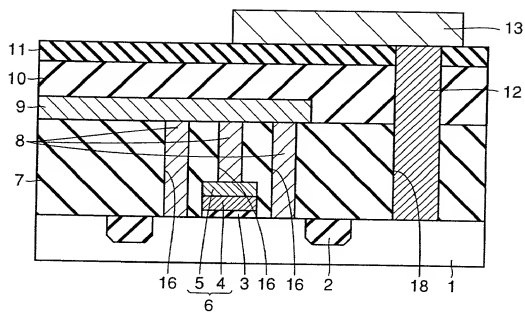


FIG. 2 PRIOR ART

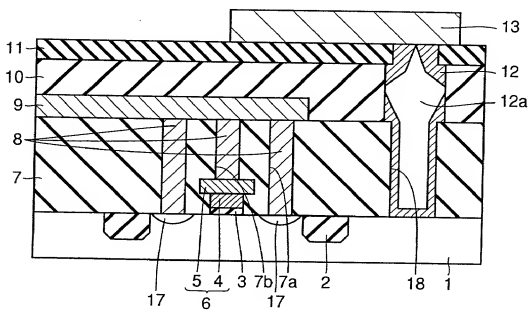


FIG. 3 PRIOR ART

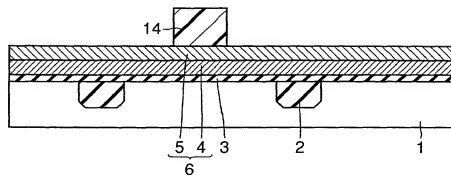


FIG. 4 PRIOR ART

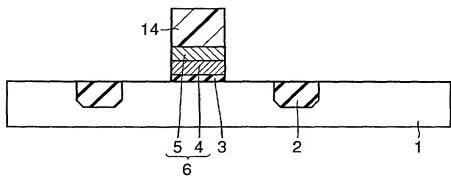


FIG. 5 PRIOR ART

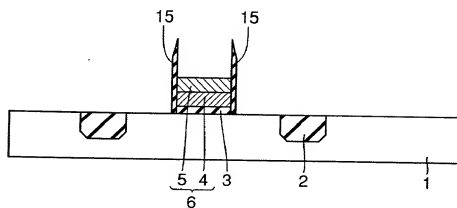


FIG. 6 PRIOR ART

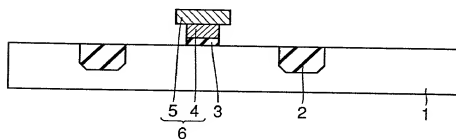


FIG. 7 PRIOR ART

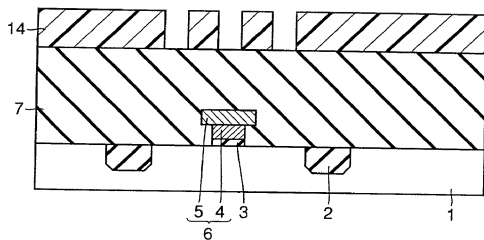


FIG. 8 PRIOR ART

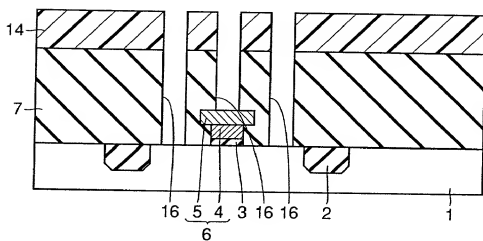


FIG. 9 PRIOR ART

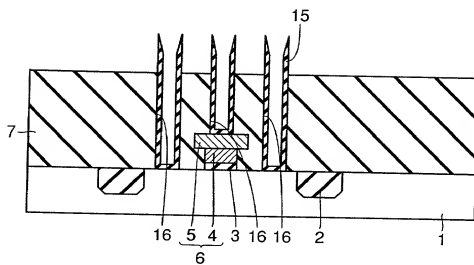


FIG. 10 PRIOR ART

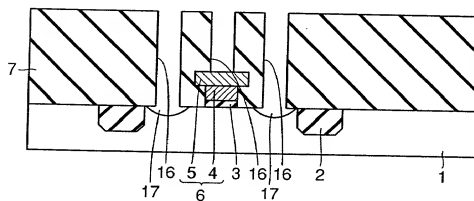


FIG. 11 PRIOR ART

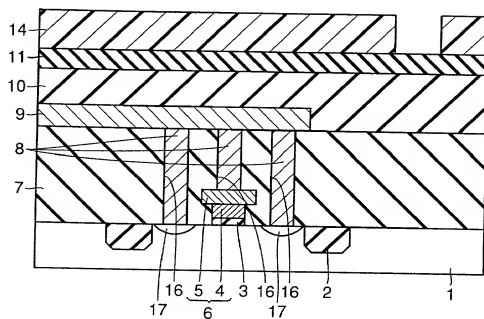


FIG. 12 PRIOR ART

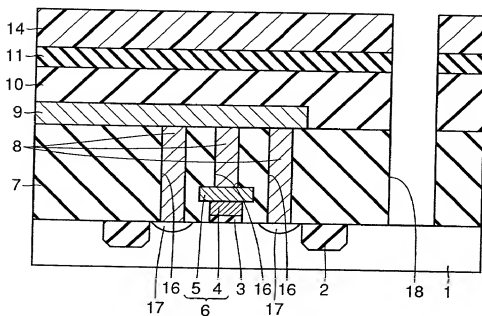


FIG. 13 PRIOR ART

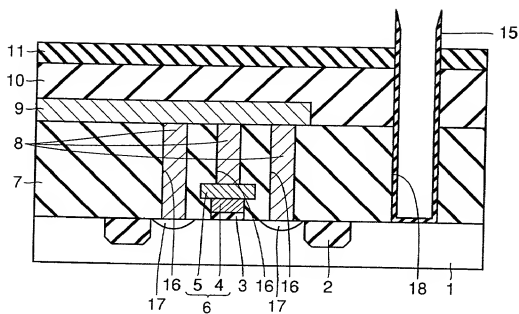


FIG. 14 PRIOR ART

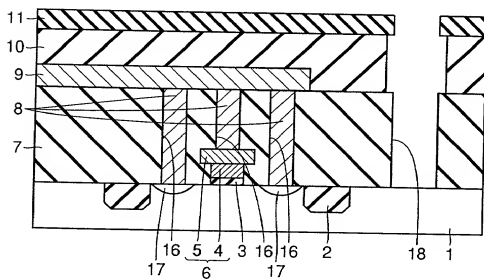
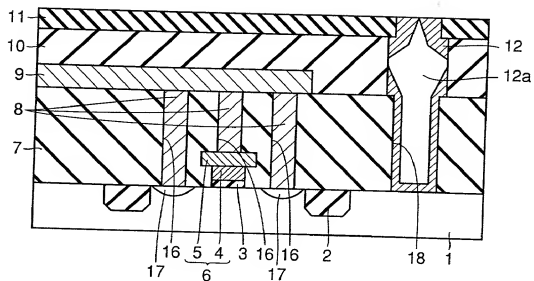


FIG. 15 PRIOR ART



Declaration and Power of Attorney For Patent Application

特許出願宣言書

Japanese Language Declaration

私は、下欄に氏名を記載した発明者として、以下のとおり宣言する：

私の住所、郵便の宛先および国籍は、下欄に氏名に続いて記載したとおりであり、

名称の発明に関し、請求の範囲に記載した特許を求める主題の本来の、最初にして唯一の発明者である（一人の氏名のみが下欄に記載されている場合）か、もしくは本来の、最初にして共同の発明者である（複数の氏名が下欄に記載されている場合）と信じ、

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

Cleaning Agent for Semiconductor Device
and Method of Fabricating Semiconductor
Device

the specification of which

(check one)

☒ is attached hereto.

☐ was filed on _____ as

Application Serial No. _____

and was amended on _____
(if applicable)

その明細書を
(該当する方に印を付す)

☒ ここに添付する。

☐ _____ 日に特許番号

第 _____ 号として提出し、

_____ 日に補正した。
(該当する場合)

私は、前記のとおり補正した請求の範囲を含む前記明細書の内容を検討し、理解したことを陳述する。

私は、連邦規則法典第37部第1章第56条(a)項に従い、本願の審査に所要の情報を開示すべき義務を有することを認める。

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

Japanese Language Declaration

私は、合衆国法典第35部第119条にもとづく下記の外国特許出願または発明者証出願の外国優先権利益を主張し、さらに優先権の主張に係わる基礎出願の出願日前の出願日を有する外国特許出願または発明者証出願を以下に明記する:

Prior foreign applications

先の外国出願

11-285515(P)	Japan	6/October/1999
(Number)	(Country)	(Day/Month/Year Filed)
(番 号)	(国 名)	(出願の年月日)
<hr/>		
(Number)	(Country)	(Day/Month/Year Filed)
(番 号)	(国 名)	(出願の年月日)
<hr/>		
(Number)	(Country)	(Day/Month/Year Filed)
(番 号)	(国 名)	(出願の年月日)

Priority claimed

優先権の主張

<input checked="" type="checkbox"/>	<input type="checkbox"/>
Yes	No
あり	なし
<hr/>	<hr/>
<input type="checkbox"/>	<input type="checkbox"/>
Yes	No
あり	なし
<hr/>	<hr/>
<input type="checkbox"/>	<input type="checkbox"/>
Yes	No
あり	なし

私は、合衆国法典第35部第120条にもとづく下記の合衆国特許出願の利益を主張し、本願の請求の範囲各項に記載の主題が合衆国法典第35部第112条第1項に規定の態様で先の合衆国出願に開示されていない限度において、先の出願の出願日と本願の国内出願日またはPCT国際出願日の間に公表された連邦規則法典第37部第1章第56条(a)項に記載の所要の情報を開示すべき義務を有することを認め

る:

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I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	(Filing Date)
(出願番号)	(出願日)
<hr/>	
(Application Serial No.)	(Filing Date)
(出願番号)	(出願日)

(現 況)	(Status)
(特許済み、係属中、放棄済み)	(patented, pending, abandoned)
<hr/>	
(現 況)	(Status)
(特許済み、係属中、放棄済み)	(patented, pending, abandoned)

私は、ここに自己の知識にもとづいて行った陳述がすべて真実であり、自己の有する情報および信ずるところに従って行った陳述が真実であると信じ、さらに故意に虚偽の陳述等を行った場合、合衆国法典第18部第1001条により、罰金もしくは禁錮に処せられるか、またはこれらの刑が併科され、またかかる故意による虚偽の陳述が本願ないし本願に対して付与される特許の有効性を損うことがあることを認識して、以上の陳述を行ったことを宣言する。

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Japanese Language Declaration

委任状: 私は、下記発明者として、以下の代理人をここに選任し、本願の手続を遂行すること並びにこれに関する一切の行為を特許商標庁に対して行うことを委任する。
(代理人氏名および登録番号を明記のこと)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (list name and registration number)

Stanislaus Aksman, Reg. No. 28,562; Edward A. Becker, Reg. No. 37,777; Stephen A. Becker, Reg. No. 26,527; William H. Beha, Reg. No. 38,035; John G. Blabakis, Reg. No. 37,095; Kenneth L. Cage, Reg. No. 26,151; Stephen C. Carlson, Reg. No. 39,929; Paul Devinsky, Reg. No. 28,553; Laura A. Donnelly, Reg. No. 38,433; Margaret M. Duncan, Reg. No. 30,879; Brian E. Ferguson, Reg. No. 36,801; Michael F. Fogarty, Reg. No. 36,139; Wilhelm F. Gadiano, Reg. No. 37,136; Keith E. George, Reg. No. 34,111; John A. Hankins, Reg. No. 32,029; Thomas A. Jolly, Reg. No. 39,241; Eric J. Kraus, Reg. No. 36,190; Edward E. Kubesiewicz, Reg. No. 30,020; Robert E. LeBlanc, Reg. No. 17,219; Jack D. Lever, Reg. No. 28,149; Raphael V. Lupo, Reg. No. 28,363; Christine F. Martin, Reg. No. 39,762; Michael E. McCabe, Jr., Reg. No. 37,182; James H. Meadows, Reg. No. 33,965; Michael A. Messing, Reg. No. 33,424; Joseph H. Paquin, Jr., Reg. No. 31,647; Craig L. Plastrik, Reg. No. 41,254; Robert L. Price, Reg. No. 22,655; Paul A. Roberts, Reg. No. 40,289; Gene Z. Robinson, Reg. No. 33,351; Joy Ann G. Serauskas, Reg. No. 27,952; Michele M. Schaffer, Reg. No. 34,717; David J. Serbin, Reg. No. 30,589; Glenn Snyder, Reg. No. 41,428; Arthur J. Steiner, Reg. No. 26,106; David L. Stewart, Reg. No. 37,578; Leonid D. Tenor, Reg. No. 39,397; Keith J. Townsend, Reg. No. 40,358; Leon R. Turkevich, Reg. No. 34,035; Christopher D. Ward, Reg. No. 41,367; Damian G. Wasserbauer, Reg. No. 34,749; Edward J. Wise, Reg. No. 34,523; Alexander V. Yampolsky, Reg. No. 36,324; and Robert W. Zelnick, Reg. No. 36,976

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Full name of sole or first inventor
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Second inventor's signature Date
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(Supply similar information and signature for third and subsequent joint inventors.)

Japanese Language Declaration

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日付		Third Inventor's signature	Date
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	Japanese		
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	Chiyoda-ku, Tokyo 100-8310 JAPAN		

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日付		Fourth Inventor's signature	Date
		<i>Naoki Ichiki</i>	September 19, 2000
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国籍	Citizenship		
	Japanese		
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	IBARAKI, JAPAN		

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同第5共同発明者の署名		Hideaki NEZU	
日付		Fifth Inventor's signature	Date
		<i>HIDEAKI NEZU</i>	September 19, 2000
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国籍	Citizenship		
	Japanese		
郵便の宛先	Post Office Address		
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	CHIBA, JAPAN		

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日付		Sixth Inventor's signature	Date
		<i>Masayuki Takashima</i>	September 19, 2000
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国籍	Citizenship		
	Japanese		
郵便の宛先	Post Office Address		
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(第7またはそれ以降の共同発明者に対しても同様な情報および署名を提供すること。)

(Supply similar information and signature for seventh and subsequent joint inventors.)